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NIT-201-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

N. KATOH et al.

Serial No. 10/084,435

Group Art Unit: 2819

Filed: February 28, 2002

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE, STORAGE MEDIUM
ON WHICH CELL LIBRARY IS STORED AND DESIGNING METHOD
FOR SEMICONDUCTOR INTEGRATED CIRCUIT

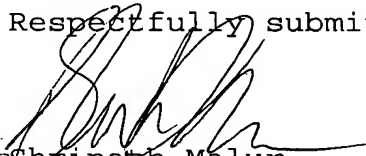
REQUEST TO CORRECT OFFICIAL FILING RECEIPT

Commissioner for Patents
Washington, D.C. 20231

Sir:

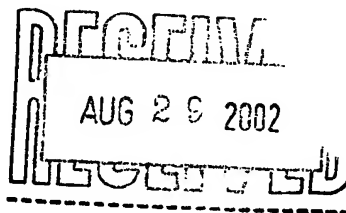
Applicants hereby request that the Domestic Priority Data as Claimed by Applicant on the Official Filing Receipt in the above-identified application be corrected. As shown on the attached stamped filing receipt, this should be a Divisional Application.

Respectfully submitted,


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Date: June 11, 2002



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Prior Application: N. KATOH et al
Serial No. 09/582,327
Filed: June 23, 2000



Group Art Unit: 2819
Examiner: A. Tran
For: SEMICONDUCTOR INTEGRATED CIRCUIT
DEVICE, STORAGE MEDIUM ON WHICH CELL
LIBRARY IS STORED AND DESIGNING METHOD
FOR SEMICONDUCTOR INTEGRATED CIRCUIT

Papers Filed Herewith:

REQUEST FOR DIVISIONAL APPLICATION
UNDER 37 C.F.R. 1.53(b);
All papers as filed in the parent application (including
Description, claims, Abstract, Declaration and
Power of Attorney, and drawings);
PRELIMINARY AMENDMENT;
INFORMATION DISCLOSURE STATEMENT, with PTO-1449 Form;
Check No. 5683 in the amount of \$740.00 in payment
of Filing Fee; and
SERIAL NO. POSTCARD.

Receipt is hereby acknowledged of the papers filed, as
identified in connection with the above-identified patent
application.

COMMISSIONER OF PATENTS AND TRADEMARKS